

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

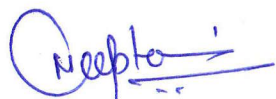
M.Tech. in Embedded System (w.e.f. July 2020)

III - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	MTRM301	Research Methodology in Engineering		3	1	0	60	20	20	0	0	100	4
2	MBAI301C	Advanced Human Values and Professional Ethics		4	0	0	60	20	20	0	0	100	4
3	MTES301	Dissertation Part I		0	0	20	0	0	0	180	120	300	10
TOTAL				7	1	20	120	40	40	180	120	500	18

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

* Students are advised to take online MOOCS course of minimum two credits



**Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore**



**Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, INDORE**



**Controller of Examinations
Shri Vaishnav Vidyapeeth Vishwavidyalaya**



**Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore**



**Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore**